

**FIG. 1**  
(PRIOR ART)

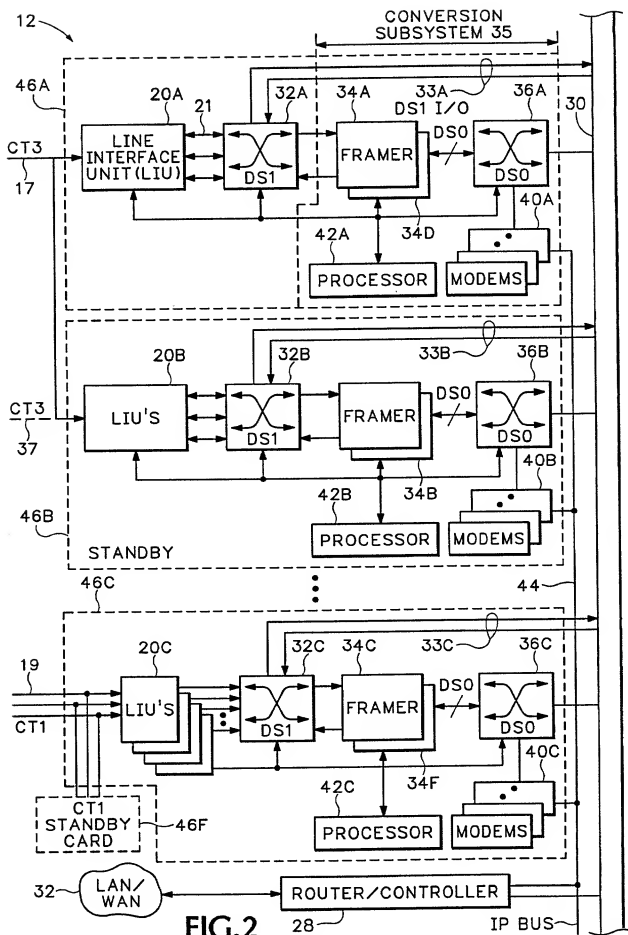
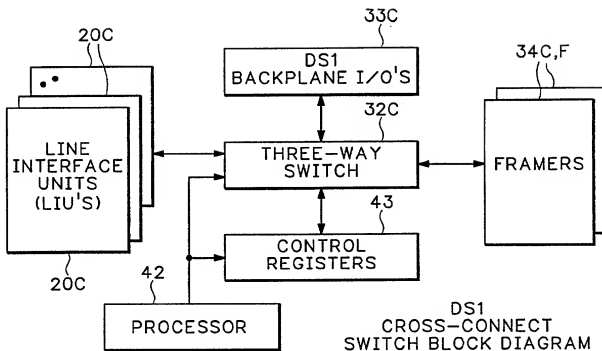
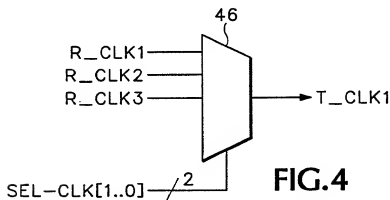


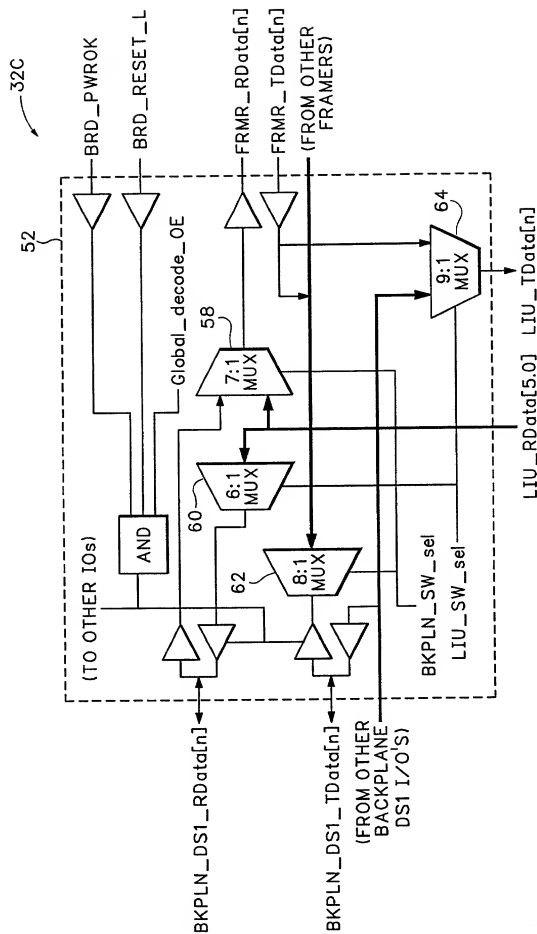
FIG. 2



**FIG.3**

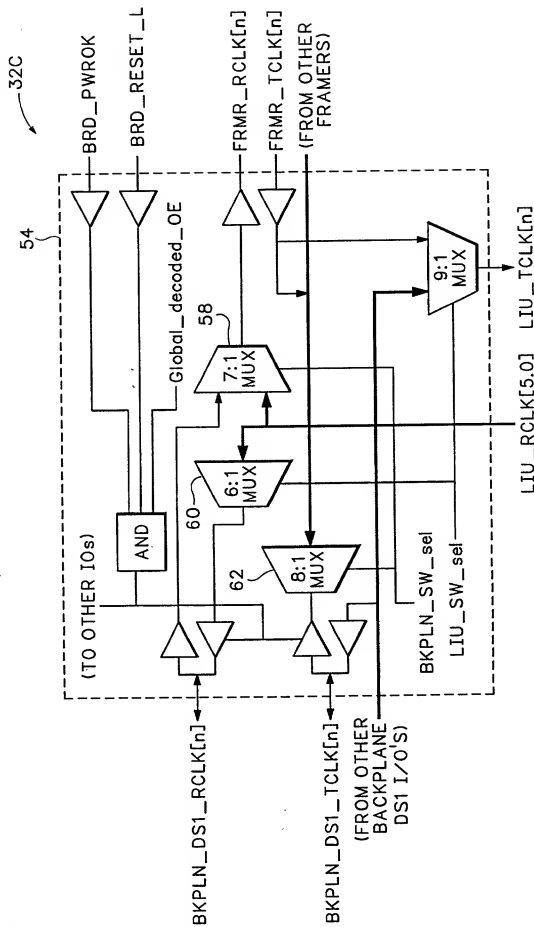


**FIG.4**



DS1 CROSS-CONNECT SWITCH

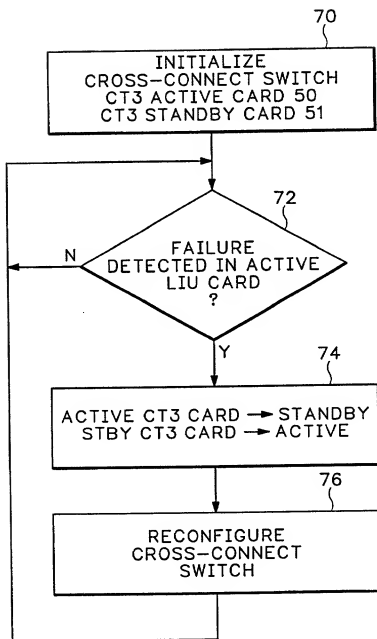
FIG. 5



DS1 CROSS-CONNECT SWITCH

**FIG. 5**

(CONTINUED)



CT3 LIU FAILURE

**FIG.6**

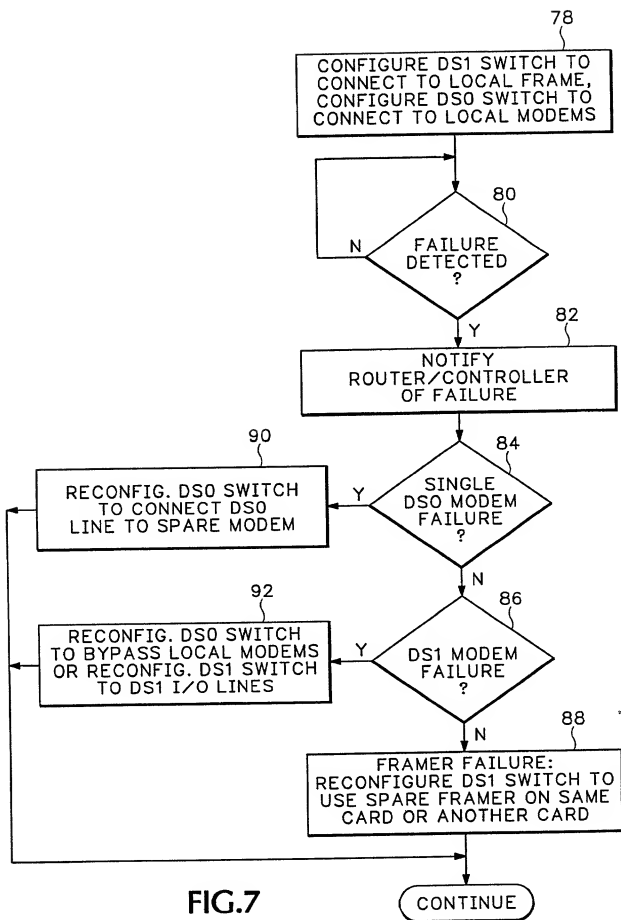


FIG.7